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TRANSMITTAL			Application / Conf. No.	10/769,663 / 8545
			Filing Date	January 29, 2004
			First Named Inventor	Soon-Shin Chee
(to be used for all correspondence after initial filing)			Examiner Name	Dana Farahani
Mail Stop: AMENDMENT			Art Unit	2891
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Attn:	Kim Kanzaki		(Customer Number)	Reg. Number 37,652
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X-1436 US 10/769,663



PATENT Conf. No.: 8545

HE UNITED STATES PATENT OFFICE

Applicants:

Soon-Shin Chee et al.

Assignee:

Xilinx, Inc.

Title:

"Integrated Circuit and Method of Producing a Carrier Wafer for an

Integrated Circuit"

Serial No.:

10/769,663

Filed:

01-29-04

Examiner:

Dana Farahani

Art Unit:

2891

Docket No.:

X-1436 US

Conf.No.:

8545

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RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT AND SUBMISSION OF AMENDED RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Notice of Non-Compliant Amendment, Applicants hereby submit the amended claim set originally sent on April 19, 2005 in response to the Restriction Requirement mailed from the Patent Office on April 8, 2005.

Respectfully submitted,

Kim/Kanzaki for Applicants

Reg. No. 37,652

I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on May 9, 2005.

Pat Slaback

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